

Purchasing DE10 Lite
It is \$64.00 with student status verification.



From:

Linda Pruett

Sr. Sales Manager

Terasic Technologies

(408)324-6826

Invent the Future of Embedded Computing with Terasic & Intel! Join the community at

www.innovatefpga.com!



[Join 2021 InnovateFPGA Design Contest! Connecting the Edge for a Sustainable Future](#)

Direct order to my e-mail address (linda@terasic.com), it is cheaper than Amazon, or our website.

We are still offering FedEx domestic 2-day shipping or USPS priority for a flat rate of \$20.00. The \$20 is per flat rate envelope, so if students want to have more than one, there will be \$3.5 per additional board (up to 4 DE10-Lite, or 2 DE10-Nano). If students have more than 4, I can give them the estimated cost of the shipping. We can ship them individually to each student's desired address or send the order in bulk to a designated location.

If you or students want to place the order, please send me (linda@terasic.com) the following information by using the official school e-mail address, or if you or the student would like to use a personal e-mail address, please send a copy of the student/faculty/staff ID.

Billing Information

Name:

Address:

City, State, Zip Code:

Phone number:

E-mail address:

Shipping Information

Name:

Address (No PO Box):

City, State, Zip Code:

Phone Number:

E-mail address:

Once we receive the information, we will send you or the student an invoice with a secure payment link. They will need to enter the amount due and invoice#.

If the student orders from the website, It will ship from our Taiwan headquarters. It is \$64+32.3 (2-4 days international shipping and plus any duties, tariffs, customs fees).

Also, it is our great pleasure to announce the 2021 InnovateFPGA. Innovate FPGA is now calling for entries! InnovateFPGA is one of the most anticipated global FPGA design contest co-hosted by Intel and Terasic. This year, we are also proud to welcome Microsoft and Analog Devices to join forces as key sponsors.

Connected devices and the data they produced are at their highest peak in human history; meanwhile, the challenges we are facing on a global scale are also greater than ever before. That's why we are inviting you to apply technologies to lessen the environmental impact and create a sustainable future.

Teams will receive a FPGA Cloud Connectivity Kit, Microsoft Azure IoT and AI, and Analog Devices Plug-in Boards for free, for the purposes of developing their projects (proposals must be selected in the first round to qualify for the previously mentioned FPGA Cloud Connectivity Kit, Microsoft Azure IoT and AI, and Analog Devices Plug-in Boards).

Teams advancing to the Grand Final will be sponsored with a round-trip flight and accommodations to present their projects to the Judging Committee at Intel San Jose.

Register and upload your proposal at the event website <http://www.innovatefpga.com/portal/index.html> by 30th September, 2021.

Please feel free to contact me if you are interested in learning more about the contest.

Students can buy DE10 Lite directly online at:

<https://www.terasic.com.tw/cgi-bin/page/archive.pl?Language=English&No=1021>